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3. The device of claim 2, wherein said at least one of the elements on the first circuit board connected to said at least one of the elements on the second circuit board via the plurality of the connecting lines is configured to receive power from the first power supply.

4. The device of claim 3, wherein said at least one of the elements that receives power from the first power supply is a processor configured to perform at least one of sending a signal or receiving the signal from said at least one of the elements on the second circuit board via the plurality of connecting lines.

5. The device of claim 2, further comprising:

a second power line formed on the non-display area of the transparent substrate,

wherein the plurality of connecting lines is disposed between the second power line and the display area.

6. The device of claim 2, wherein the plurality of connecting lines includes a plurality of signal lines.

7. The device of claim 6, the plurality of the signal lines are disposed on a same layer and spaced apart from one another.

8. The device of claim 6, wherein at least one of the signal lines is disposed on a different layer from another signal line without being overlapped by one another.

9. The device of claim 6, wherein a width of each of the signal lines is equal to or less than 100 μm .

10. The device of claim 6, wherein the plurality of the connecting lines further include at least one detection signal line, wherein the detection signal line is disposed on a different layer from a layer where said one or more signal lines are disposed on.

11. The device of claim 10, wherein the detection signal line and said one or more signal lines are arranged such that at least some part of the detection signal line overlaps with some of the signal lines.

12. The device of claim 11, wherein a width of the detection signal line is equal to or less than 1000 μm .

13. The device of claim 2, further comprising:

a thin-film transistor formed on the display area of the transparent substrate, the thin-film transistor having an active layer, a gate electrode, a source electrode, and a drain electrode,

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wherein at least one of the connecting lines is made of the same material as the gate electrode, and wherein at least one of the connecting lines is made of the same material as the source electrode or the drain electrode.

14. A transparent display device, comprising:

a transparent substrate having a display area and a non-display area adjacent to the display area;

a first substrate and a second substrate, each connected to a different side of the transparent substrate; and

a plurality of connecting lines on the non-display of the transparent substrate, the plurality of connecting lines electrically connecting a first element on the first substrate and a second element on the second substrate.

15. The device of claim 14, wherein the plurality of the connecting lines are disposed on a same layer and spaced apart from one another.

16. The device of claim 14, wherein the plurality of the connecting lines are disposed on two or more different layers without being overlapped with one another.

17. The device of claim 14, wherein the plurality of the connecting lines are disposed on two or more different layers, and wherein at least one of the connecting lines on one layer at least partially overlaps with another connecting line on another layer.

18. The device of claim 14, further comprising:

a first power supply disposed on the first substrate; and an auxiliary power line formed on the non-display area of the transparent substrate providing power to the first power supply.

19. The device of claim 18, further comprising:

a second power supply disposed on the second substrate, wherein the second power supply is configured to supply power to the first power supply via the auxiliary power line.

20. The device of claim 18, wherein a part of the auxiliary power line overlaps with at least some of the plurality of connecting lines.

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